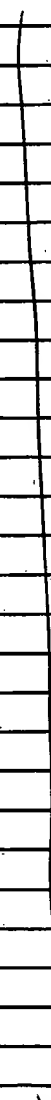

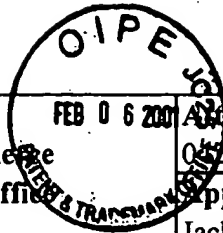
 US Department of Commerce Patent and Trademark Office Form PTO-1449 (Modified)				Atty. Docket No.: 042390.P1674C2D4		Application No.: 09/707,486	
				Applicant: Pippin, J.			
				Filing Date: November 7, 2000			
US Patent Documents							
Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date	
<i>M.S.</i> 		4,432,031	Premierlani	361	97		
		4,442,972	Sahay et al.	236	1EA		
		4,488,824	Salem	374	181		
		5,077,491	Heck et al.	307	354		
		5,291,607	Ristic et al.	395	750		
		4,789,819	Nelson	323	314		
		5,287,292	Kennefal	364	550		
		5,255,149	Matsuo	361	103		
		5,560,017	Barrett et al.	395	733		
		5,440,305	Signore et al.	341	120		
		5,253,938	Stixrud	374	173		
		4,591,855	Blackburn	340	870.13		
		5,025,248	Bergeron	340	596		
		5,064,296	Huijsing et al.	374	163		
		4,751,405	Bufano Jr. et al.	307	350		
		5,008,771	Palera	361	103		
		4,807,144	Joehlin et al.	364	473		
		H562	Trachier et al.				
		Re. 32,960	Levine				
		Re. 34,789	Fraden				
		4,779,161	DeShazo, Jr.	361	106		
		4,851,987	Day	395	560		
		4,924,112	Anderson et al.	326	32		
		5,105,366	Beckey	364	505		
		5,453,682	Hinrichs et al.	324	132		
		4,799,176	Cacciotore	364	557		
		5,287,292	Kenney et al.	364	550		
		5,451,892	Bailey	327	113		
	4,787,007	Matsumura et al.	364	98			
	4,903,106	Fukunaga et al.	357	43			
Examiner 			Date Considered 12/1/04				

-----Examiner:--Initial if reference is considered; whether or not citation is in conformance with MPEP 609; Draw a line through the citation if not in conformance and not considered. Include a copy of this form with the next communication to the applicant

US Department of Commerce Patent and Trademark Office		Atty. Docket No.: 042390.P1674C2D4		Application No.: 09/707,486	
Office PTO-1449 (Modified) <div style="text-align: center;"> US Patent Documents </div>		Applicant: Pippin, J.			
		Filing Date: November 7, 2000			

Examiner's Initials	Date	Document Number	Name	Class	Sub-Class	Filing Date
M.S. ↓		4,935,864	Schmidt et al.	363	141	
		5,087,870	Salesky et al.	323	276	
		5,149,199	Kinoshita	374	175	
		5,170,344	Berton et al.	364	400	
		5,283,631	Koerner et al.	307	451	
		5,325,256	Weng et al.	364	557	
		5,359,236	Giordano et al.	307	310	
Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)						
M.S.		Chin, Shu-Yuan and Chung-Yu Wu, "A New Type of Curvature-Compensated CMOS Bandgap Voltage References, VLSITSA, 1991, pps. 398-402.				
↓		Ferro, Marco, Franco Salerno and Rinaldo Castello, "A Floating CMOS Bandgap Voltage Reference for Differential Applications", IEEE: Journal of Solid-State Circuits, Vol. 24, No. 3, June 1989, pps. 690-697.				
↓		Salminen, O. and K. Halonen, "The Higher Order Temperature Compensation of Bandgap Voltage References", IEEE, 1992, pps. 1388-1391.				
↓		Allen and Holberg, "CMOS Analog Circuit Design", H.P.W., pp. 539-549, 1987.				
↓		Analog Devices, Low Power, Programmable Temperature Controller, pps. 1-16, 1995				
↓		Sanchez et al., "Thermal Management System for High Performance PowerPC Microprocessors", pps. 325-330, 1997.				
Examiner 		Date Considered 12/1/04				

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US Department of Commerce Patent and Trademark Office	App. Docket No.: 052390.P1674C2D4	Application No.: 09/707,486
	Applicant: Jack D. Pippin	
Form PTO-1449 (Modified)	Filing Date: November 7, 2000	

US Patent Documents

Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date

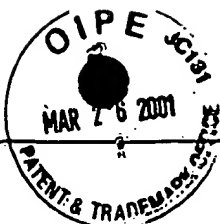
Foreign Patent Documents

Examiner's Initials	Date	Document Number	Country	Class	Sub- Class	Translation
M.S.	1/20/93	0523736A1	European	G01R	31/28	

Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)

Examiner <i>Messa Shuchat</i>	Date Considered <i>12/1/04</i>
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US Department of Commerce Patent and Trademark Office Form PTO-1449 (Modified)		Atty. Docket No.: 42390P1674C2D4		Application No.: 09/707,486		
		Applicant: Pippin, J.				
		Filing Date: November 7, 2000				
Examiner's Initials	Date	Document Number	Name	Class	Sub- Class	Filing Date
M.S.		5,085,526	Sawtell et al.	374	101	
		5,195,827	Audy et al.	374	172	
		5,586,270	Rotier et al.	395	282	
		5,189,314	Georgiou et al.	307	271	
		5,359,234	Atriss et al.	307	310	
		5,422,832	Moyal	364	557	
		5,483,102	Neal et al.	257	712	
		5,838,578	Pippin, J.	364	488	
		5,490,059	Mahalingaiah, et al.	364	166	
		5,723,998	Saito, et al.	327	513	
		5,485,127	Bertoluzzi et al.	331	69	
		4,893,097	Zwack	331	176	
		5,798,667	Herbert	327	513	
Examiner's Initials	Date	Document Number	Country	Class	Sub-Class	Translation
M.S.		0683558A1	EPO			
<i>M. S. Shultz</i>			Date Considered 12/1/04			

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Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. 238663US25DIV		SERIAL NO. 09/707,448	
<div style="border: 1px solid black; border-radius: 50%; padding: 10px; display: inline-block;"> LIST OF REFERENCES CITED BY APPLICANT NOV 07 2003 </div>				APPLICANT Jack D PIPPIN			
				FILING DATE November 7, 2000		GROUP 2824	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
AA							
AB							
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FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)							
M.S.	AW	Intel Corporation, "Advanced Power Management (APM)- BIOS Interface Specification," Revision 1.2, February 1996					
	AX						
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner <i>M. S. Pippin</i>				Date Considered 12/1/04			
<small>*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							